

# Enhanced Power Quality and Sustainable Energy with a Grid-Tied Three-Phase Solar PV Integrated Unified Power Quality Conditioner (PV-UPQC)

<sup>1</sup>Anuj Kumar and <sup>2</sup>Pramod Kumar Rathore,

<sup>1</sup>M.Tech Scholar, <sup>2</sup>Assistant Professor,

<sup>1,2</sup>RKDF College of Engineering, Bhopal, Madhya Pradesh, India

**Abstract**—This work focuses on the design and analysis of a three-phase single stage solar photovoltaic integrated unified power quality conditioner (PV-UPQC). The PV-UPQC is composed of shunt and series voltage compensators that are interconnected in a back-to-back configuration via a shared DC-link. The shunt compensator serves the twin purpose of harvesting electricity from the photovoltaic (PV) array and correcting for harmonics in the load current. A more advanced synchronous reference frame control, using a moving average filter, is used to extract the load's active current component, resulting in enhanced performance of the PV-UPQC. The series compensator is designed to mitigate power quality issues on the grid side, specifically addressing problems such as voltage sags and swells. The compensator introduces voltage that is either in-phase or out of phase with the point of common coupling (PCC) voltage, depending on whether there is a sag or swell situation, respectively. The suggested system integrates the advantages of sustainable energy production while enhancing power quality. The system's steady state and dynamic performance are assessed by conducting simulations in MATLAB-Simulink with a nonlinear load. The system performance is further validated by conducting experiments on a reduced-scale laboratory prototype, subjecting it to various disturbances like load imbalances, voltage sags/swells at the point of common coupling (PCC), and variations in irradiation.

**Keywords** — *Power Quality, shunt compensator, series compensator, UPQC, Solar PV, MPPT.*

## I. INTRODUCTION

Due to the progress in semiconductor technology, power electronic loads are becoming more prevalent. These devices, such as computer power supplies, changeable speed drives, and switching mode power supplies, exhibit high efficiency yet use non-linear currents. The presence of nonlinear currents in distribution systems leads to voltage distortion at the point of common coupling. There is a growing focus on generating sustainable energy by installing rooftop photovoltaic (PV) systems in both small residences and commercial buildings [1], [2]. However, the sporadic nature of photovoltaic (PV) energy sources may cause voltage quality issues such as voltage sags and swells. This becomes more pronounced when there is a higher adoption of PV systems, especially in less robust distribution systems, ultimately resulting in grid instability. The range is from 3 to 7. These voltage quality issues may cause power electronic systems to trip falsely, electronic systems to fail or trigger falsely, and capacitor banks to overheat. The range is from 8 to 10. Modern distribution systems have significant challenges in terms of power quality concerns, which arise from both the load side and the grid side. Given the increasing need for clean energy and the strict power quality standards of advanced electronic devices, there is a need for multifunctional systems that can combine clean

energy production with power quality enhancement. In [11], [12], a proposed three-phase multi-functional solar energy conversion system addresses load side power quality challenges. An article in [13], [14] introduces a solar PV converter that operates in a single phase and has the capacity to actively filter electricity. Significant research has been conducted on the integration of sustainable energy production with shunt active filtering. While shunt active filtering has the capacity to regulate load voltage, it does so by injecting reactive power. Therefore, shunt active filtering is unable to simultaneously adjust the voltage at the point of common coupling (PCC) and maintain a power factor of unity for the grid current. In light of the strict voltage quality standards for advanced electronic devices, the implementation of series active filters has been suggested for use in small residences and commercial buildings [15], [16]. In [17], a proposal was made for the integration of a solar photovoltaic system with a dynamic voltage restorer. A unified power quality conditioner (UPQC) is capable of simultaneously regulating load voltage and maintaining grid current sinusoidal at unity power factor. Unlike shunt and series active power filters, the UPQC achieves this by including both series and shunt compensators. Combining a photovoltaic (PV) array with a unified power quality conditioner (UPQC) provides the advantages of both producing clean energy and achieving universal active power. The combination of photovoltaic (PV) array with unified power quality conditioner (UPQC) has been documented in references [18]–[20]. The solar PV integrated UPQC has many advantages over traditional grid linked inverters. These include enhancing the power quality of the grid, safeguarding essential loads from disruptions originating from the grid, and boosting the converter's capacity to withstand transients during faults. Due to the growing focus on distributed generation and microgrids, there is a revived interest in UPQC systems [21], [22].

Generating a reference signal is a significant undertaking in controlling PV-UPQC. The strategies for generating reference signals may be categorized into two basic types: time-domain techniques and frequency-domain techniques [8]. Time domain methods are often used due to their reduced processing demands during real-time execution. Commonly used methodologies include instantaneous reactive power theory (p-q theory), synchronous reference frame theory (d-q theory), and instantaneous symmetrical component theory [23]. An inherent drawback of using the synchronous reference frame theory-based technique is the presence of a double harmonic component in the d-axis current under load imbalanced conditions. As a result, low pass filters with an extremely low cutoff frequency are used to eliminate the double harmonic component. This leads to inadequate dynamic performance [24]. This study utilizes a moving average filter (MAF) to effectively filter the d-axis current and extract the basic load

active current. This achieves maximum attenuation while maintaining the controller's bandwidth intact [25]. MAF has recently been used to enhance the performance of DC-link controllers and to achieve grid synchronization via the use of phase locked loop (PLL). The references [26] and [27] are provided.

This work presents the design and performance analysis of a three-phase PV-UPQC. A control system based on MAF (Moving Average Filter) and d-q theory is used to enhance the dynamic performance while extracting active current during load operation. The primary benefits of the suggested system are as follows:

- Integration of clean energy generation and power quality improvement.
- Simultaneous voltage and current quality improvement.
- Improved load current compensation due to use of MAF in d-q control of PV-UPQC.
- Stable under various dynamic conditions of voltage sags/swells, load unbalance and irradiation variation.

The performance of the proposed system is analysed extensively under both dynamic and steady state conditions using MATLAB-Simulink software. The performance is then experimentally verified using a scaled down laboratory prototype under various conditions experienced in the distribution system such as voltage sags/swells, load unbalance and irradiation variation.

## II. UNIFIED POWER QUALITY CONDITIONER (UPQC)

The Unified Power Quality Conditioner (UPQC) is a versatile power conditioner that may be used to compensate for various voltage disturbances and changes in the power source, as well

as to block the entry of harmonic load current into the power system [28]. This is a specialized power device designed to mitigate the impact of disruptions on the functioning of delicate equipment. The Unified Power Quality Conditioner (UPQC) consists of two voltage-source inverters connected by a shared direct current (dc) connection. The dc connection may be configured as single-phase, three-phase three-wire, or three-phase four-wire [29]. In the active power filter (APF) series, one inverter is regulated as a variable voltage source, while another inverter is regulated as a variable current source in the shunt active power filter (APF). The series Active Filter compensates for power supply anomalies such as harmonics, imbalances, sag, swell, flickers, negative and zero sequence components. The shunt filter is designed to compensate for load current distortions, such as those caused by harmonics or imbalances, as well as to regulate reactive power and manage the dc link voltage [30].

## III. System Configuration and Design of UPQC

Figure 1 depicts the PV-UPQC design. The topology is specifically developed for a three-phase system. The system layout included a set of series converters and shunt converters connected in a back-to-back arrangement, with a DC-bus located between them. Typically, the shunt converters are linked during the load end. The Solar Photovoltaic array will be linked directly to the UPQC's common DC-link capacitor using a diode that may impede reverse current flow. The series converter typically operates in voltage control mode and is designed to mitigate grid voltage sags and swells. Inductors are used to link a shunt converter and a series converter to a network or electrical grid. A series-connected injection transformer may be used to inject a voltage signal, created by a series-connected compensator, into an electrical grid system. Ripple filters are designed to eliminate the harmonics that are produced as a result of the rapid switching of the converters. A nonlinear load is used, consisting of a bridge rectifier linked to a voltage-fed load.

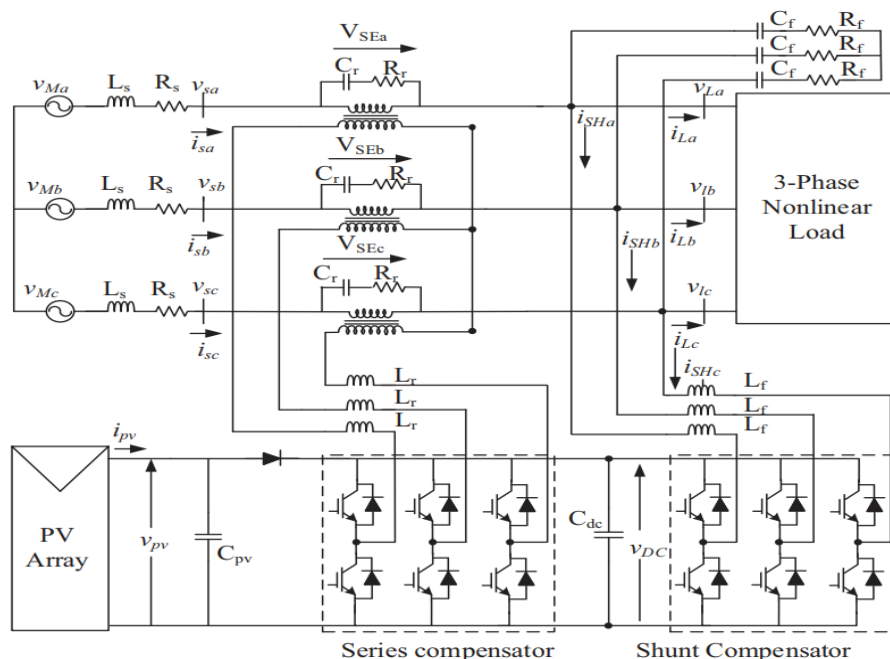


Figure1: System Configuration of PV-UPQC

### A. Basics of design configuration of PV-UPQC

The design process of the PV-UPQC begins with accurately determining the size of the solar photovoltaic array, the voltage

level of the DC-Link, and the capacitor for the DC-Link. The selection of the shunt converter's size is crucial to effectively handle the highest power output generated by the solar

Photovoltaic array, while also compensating for reactive power and current harmonics. The photovoltaic solar array must be coupled with the system via a common DC link of the Unified Power Quality Conditioner (UPQC). Therefore, while determining the size of the PV array, it is important to verify that the maximum power point (MPP) voltage meets the needed voltage at the DC link. When choosing the power rating, it is crucial to note that a solar array should be able to provide the active power required by the load under typical circumstances, while also being able to feed excess power back into the energy grid. The additional components include an interface inductor linked in series, as well as shunt compensators. There is also a series injecting transformer connected to a series connected compensator.

**IV. SIMULATION RESULTS AND DISCUSSION**

Figure 2 shows the MATLAB Model of PV Integrated UPQC system. By performing simulation study in MATLAB software, a steady state as well as dynamic system behavior of Solar PV-UPQC can be examined. A load having nonlinear nature is used which comprises of 3Ph diode bridge connected rectifier connected with R-L load. A stepping size for simulation study is taken as 1e-6s. There are different dynamic conditions occurring in the system which may be voltage sag and voltage swells at PCC end and variation in the Photovoltaic irradiation.

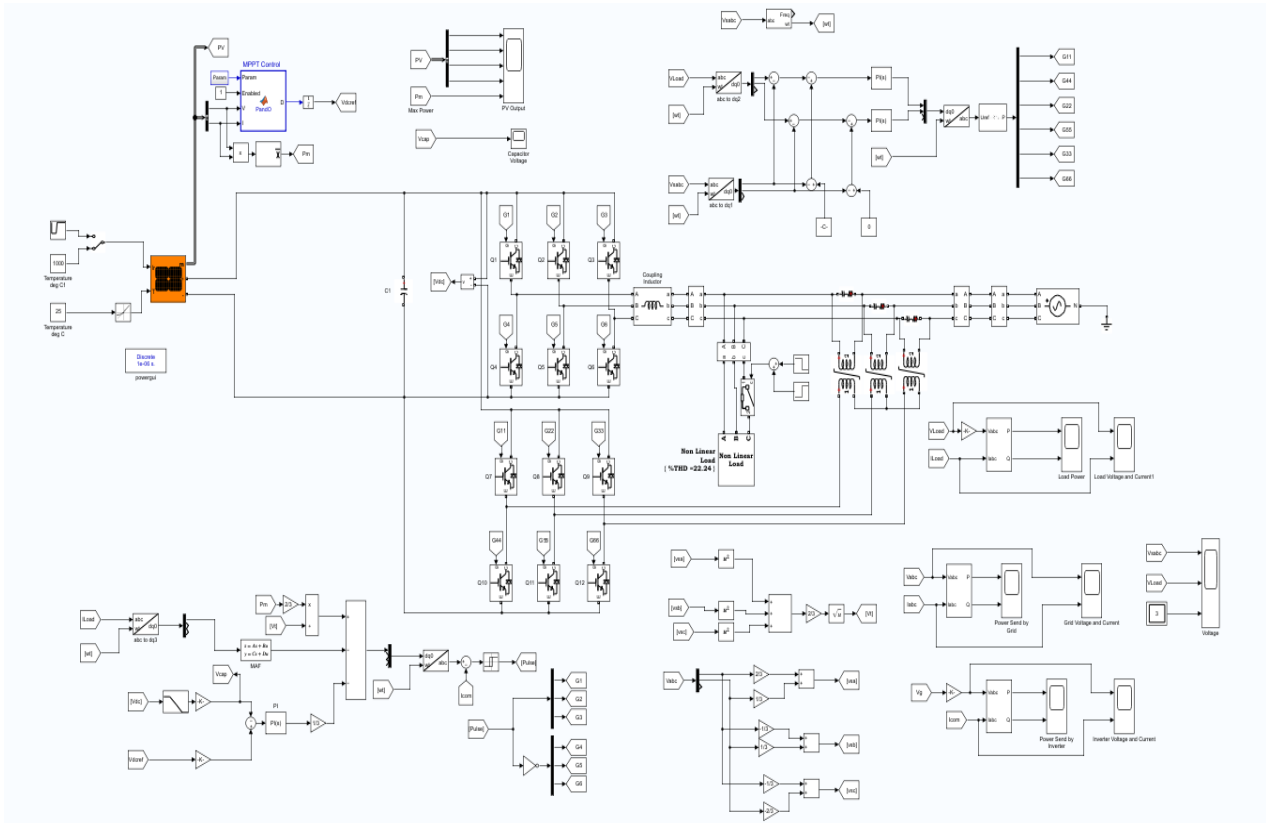


Figure 2: MATLAB Model of PV Integrated UPQC system

**TABLE I: SIMULATION PARAMETERS**

Simulation Parameters	Values
Maximum Power (W)	213.15
Cells per module (Ncell)	60
Open circuit voltage Voc (V)	36.3
Short-circuit current Isc (A)	7.84
Voltage at maximum power point Vmp (V)	29
Current at maximum power point Imp (A)	7.35
Parallel Strings	18
Series-connected modules per string	25

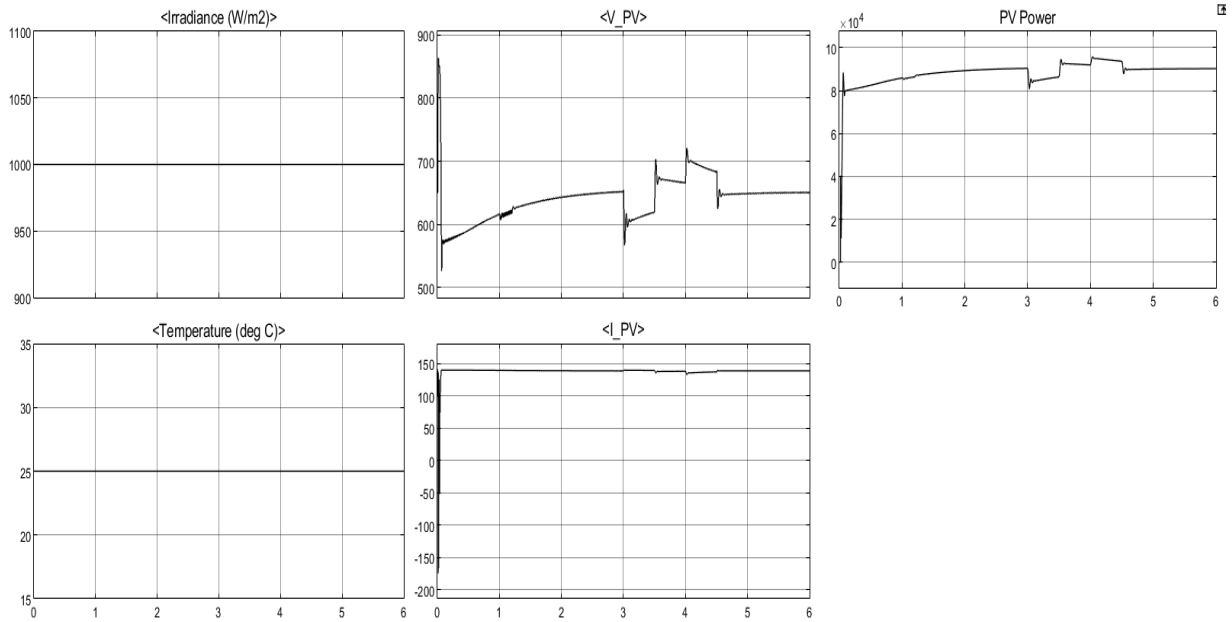


Figure 3: Irradiance, Temperature, Voltage-PV, Current-PV & Power-PV V/s Time in (S)

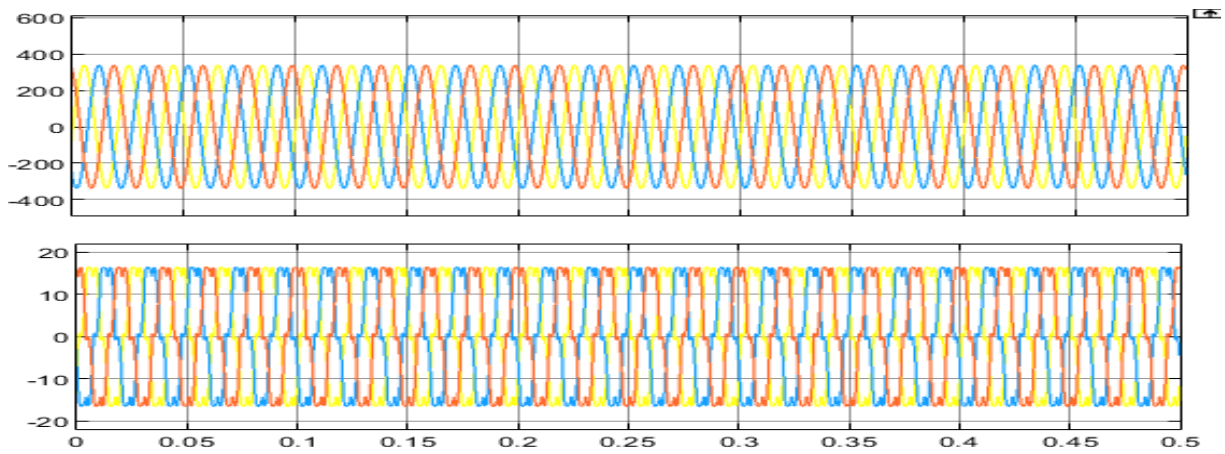


Figure 4: Load Voltage and Current V/s Time in (S)

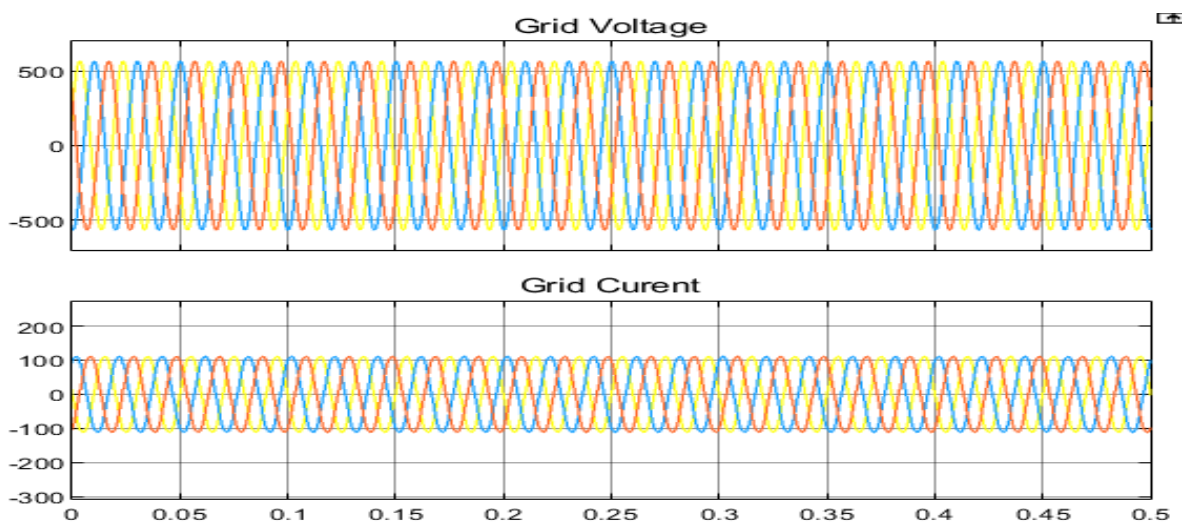


Figure 5: Grid Voltage and Grid Current V/s Time in (S)

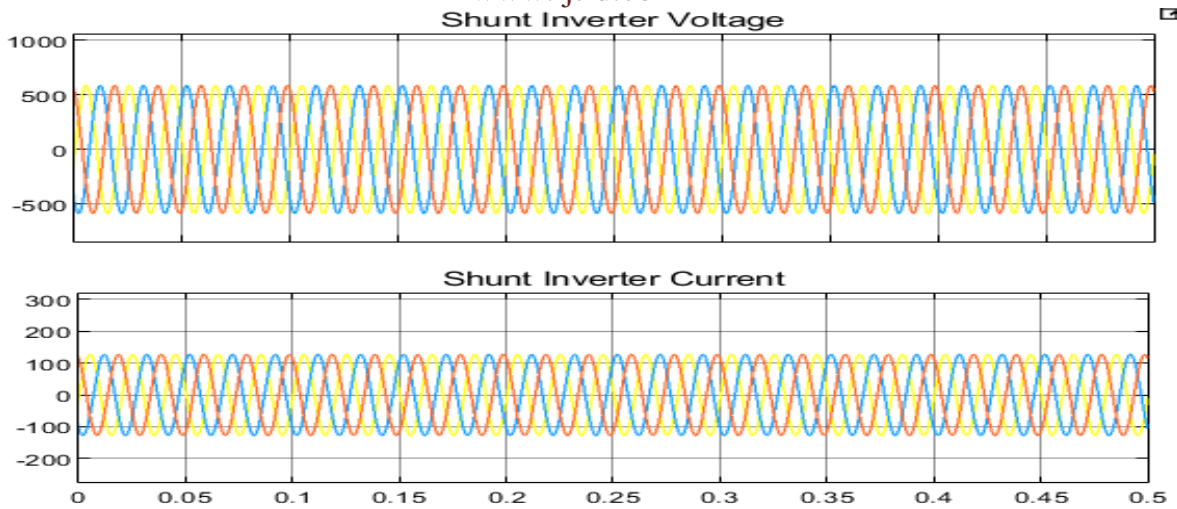


Figure 6: Shunt Inverter Voltage and Shunt Inverter Current V/s Time in (S)

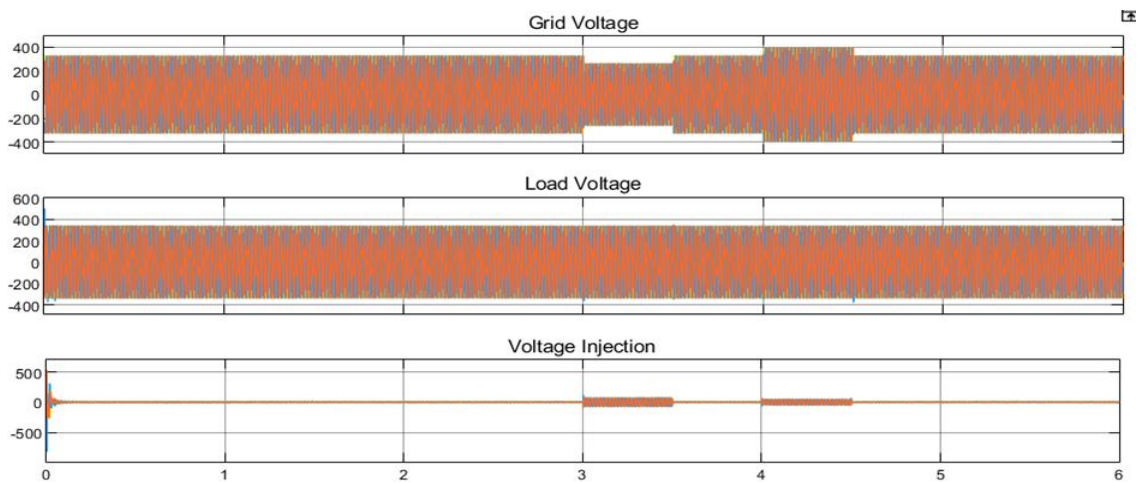


Figure 7: Grid Voltage, Load Voltage and Voltage Injection V/s Time in (S)

## CONCLUSION

An analysis has been conducted on the design and dynamic performance of a three-phase PV-UPQC, including changing irradiation and grid voltage sags/swells. The system's performance has been verified by conducting experiments on a reduced-scale laboratory prototype. PV-UPQC has been shown to effectively reduce the harmonics generated by nonlinear loads and ensure that the total harmonic distortion (THD) of the grid current remains within the limitations specified by the IEEE-519 standard. The system demonstrates stability when exposed to changes in irradiation, voltage sags/swell, and load imbalance. The use of a moving average filter has enhanced the effectiveness of d-q control, especially in situations when the load is uneven. PV-UPQC is an effective option for current distribution systems as it combines distributed generating with power quality enhancement.

## References

- [1] B. Mountain and P. Szuster, "Solar, solar everywhere: Opportunities and challenges for australia's rooftop pv systems," *IEEE Power and Energy Magazine*, vol. 13, no. 4, pp. 53–60, July 2015.
- [2] A. R. Malekpour, A. Pahwa, A. Malekpour, and B. Natarajan, "Hierarchical architecture for integration of rooftop pv in smart distribution systems," *IEEE Transactions on Smart Grid*, vol. PP, no. 99, pp. 1–1, 2017.
- [3] Y. Yang, P. Enjeti, F. Blaabjerg, and H. Wang, "Wide-scale adoption of photovoltaic energy: Grid code modifications are explored in the distribution grid," *IEEE Ind. Appl. Mag.*, vol. 21, no. 5, pp. 21–31, Sept 2015.
- [4] M. J. E. Alam, K. M. Muttaqi, and D. Sutanto, "An approach for online assessment of rooftop solar pv impacts on low-voltage distribution networks," *IEEE Transactions on Sustainable Energy*, vol. 5, no. 2, pp. 663–672, April 2014.
- [5] J. Jayachandran and R. M. Sachithanandam, "Neural network-based control algorithm for DSTATCOM under nonideal source voltage and varying load conditions," *Canadian Journal of Electrical and Computer Engineering*, vol. 38, no. 4, pp. 307–317, Fall 2015.
- [6] A. Parchure, S. J. Tyler, M. A. Peskin, K. Rahimi, R. P. Broadwater, and M. Dilek, "Investigating pv generation induced voltage volatility for customers sharing a distribution service transformer," *IEEE Trans. Ind. Appl.*, vol. 53, no. 1, pp. 71–79, Jan 2017.
- [7] E. Yao, P. Samadi, V. W. S. Wong, and R. Schober, "Residential demand side management under high penetration of rooftop photovoltaic units," *IEEE Transactions on Smart Grid*, vol. 7, no. 3, pp. 1597–1608, May 2016.
- [8] B. Singh, A. Chandra and K. A. Haddad, *Power Quality: Problems and Mitigation Techniques*. London: Wiley, 2015.
- [9] M. Bollen and I. Guo, *Signal Processing of Power Quality Disturbances*. Hoboken: John Wiley, 2006.
- [10] P. Jayaprakash, B. Singh, D. Kothari, A. Chandra, and K. Al-Haddad, "Control of reduced-rating dynamic voltage restorer with a battery energy storage system," *IEEE Trans. Ind. Appl.*, vol. 50, no. 2, pp. 1295–1303, March 2014.
- [11] B. Singh, C. Jain, and S. Goel, "ILST control algorithm of single-stage dual purpose grid connected solar pv system," *IEEE Trans. Power Electron.*, vol. 29, no. 10, pp. 5347–5357, Oct 2014.
- [12] R. K. Agarwal, I. Hussain, and B. Singh, "Three-phase single-stage grid tied solar pvecs using PLL-less fast CTF control technique," *IET Power Electronics*, vol. 10, no. 2, pp. 178–188, 2017.
- [13] Y. Singh, I. Hussain, B. Singh, and S. Mishra, "Single-phase solar grid-interfaced system with active filtering using adaptive linear combiner filter-based control scheme," *IET Generation, Transmission Distribution*, vol. 11, no. 8, pp. 1976–1984, 2017.

- [14] T.-F. Wu, H.-S. Nien, C.-L. Shen, and T.-M. Chen, "A single-phase inverter system for pv power injection and active power filtering with nonlinear inductor consideration," *IEEE Trans. Ind. Appl.*, vol. 41, no. 4, pp. 1075–1083, July 2005.
- [15] A. Javadi, A. Hamadi, L. Woodward, and K. Al-Haddad, "Experimental investigation on a hybrid series active power compensator to improve power quality of typical households," *IEEE Trans. Ind. Electron.*, vol. 63, no. 8, pp. 4849–4859, Aug 2016.
- [16] A. Javadi, L. Woodward, and K. Al-Haddad, "Real-time implementation of a three-phase thseaf based on vsc and p+r controller to improve power quality of weak distribution systems," *IEEE Transactions on Power Electronics*, vol. PP, no. 99, pp. 1–1, 2017.
- [17] A. M. Rauf and V. Khadkikar, "Integrated photovoltaic and dynamic voltage restorer system configuration," *IEEE Transactions on Sustainable Energy*, vol. 6, no. 2, pp. 400–410, April 2015.
- [18] S. Devassy and B. Singh, "Design and performance analysis of three-phase solar pv integrated upqc," in *2016 IEEE 6th International Conference on Power Systems (ICPS)*, March 2016, pp. 1–6.
- [19] K. Palanisamy, D. Kothari, M. K. Mishra, S. Meikandashivam, and I. J. Raglend, "Effective utilization of unified power quality conditioner for interconnecting PV modules with grid using power angle control method," *International Journal of Electrical Power and Energy Systems*, vol. 48, pp. 131–138, 2013.
- [20] S. Devassy and B. Singh, "Modified p-q theory based control of solar pv integrated upqc-s," *IEEE Trans. Ind. Appl.*, vol. PP, no. 99, pp. 1–1, 2017.
- [21] S. K. Khadem, M. Basu, and M. F. Conlon, "Intelligent islanding and seamless reconnection technique for microgrid with upqc," *IEEE Journal of Emerging and Selected Topics in Power Electronics*, vol. 3, no. 2, pp. 483–492, June 2015.
- [22] J. M. Guerrero, P. C. Loh, T. L. Lee, and M. Chandorkar, "Advanced control architectures for intelligent microgrids; part ii: Power quality, energy storage, and ac/dc microgrids," *IEEE Transactions on Industrial Electronics*, vol. 60, no. 4, pp. 1263–1270, April 2013.
- [23] B. Singh and J. Solanki, "A comparison of control algorithms for dstatcom," *IEEE Transactions on Industrial Electronics*, vol. 56, no. 7, pp. 2738–2745, July 2009.
- [24] B. Singh, C. Jain, S. Goel, A. Chandra, and K. Al-Haddad, "A multifunctional grid-tied solar energy conversion system with anf-based control approach," *IEEE Transactions on Industry Applications*, vol. 52, no. 5, pp. 3663–3672, Sept 2016.
- [25] S. Golestan, M. Ramezani, J. M. Guerrero, and M. Monfared, "dq-frame cascaded delayed signal cancellation-based pll: Analysis, design, and comparison with moving average filter-based pll," *IEEE Transactions on Power Electronics*, vol. 30, no. 3, pp. 1618–1632, March 2015.
- [26] R. Pea-Alzola, D. Campos-Gaona, P. F. Ksiazek, and M. Ordonez, "Dc-link control filtering options for torque ripple reduction in low-power wind turbines," *IEEE Trans. Power Electron.*, vol. 32, no. 6, pp. 4812–4826, June 2017.
- [27] S. Golestan, M. Ramezani, J. M. Guerrero, F. D. Freijedo, and M. Monfared, "Moving average filter based phase-locked loops: Performance analysis and design guidelines," *IEEE Trans. Power Electron.*, vol. 29, no. 6, pp. 2750–2763, June 2014.
- [28] B. Subudhi and R. Pradhan, "A comparative study on maximum power point tracking techniques for photovoltaic power systems," *IEEE Transactions on Sustainable Energy*, vol. 4, no. 1, pp. 89–98, Jan 2013.
- [29] A. Sadigh and K. Smedley, "Review of voltage compensation methods in dynamic voltage restorer DVR," in *IEEE Power and Energy Society General Meeting*, July 2012, pp. 1–8.
- [30] A. Rauf and V. Khadkikar, "An enhanced voltage sag compensation scheme for dynamic voltage restorer," *IEEE Trans. Ind. Electron.*, vol. 62, no. 5, pp. 2683–2692, May 2015.